

Notice of References Cited	Application/Control No. 10/707,860	Applicant(s)/Patent Under Reexamination HOU ET AL.	
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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,769,035	07-2004	Garreau, Oliver F.	710/14
*	B	US-2005/0120153	06-2005	Perez et al.	710/062
*	C	US-2005/0034045	02-2005	Lueck et al.	714/746
*	D	US-2004/0003154	01-2004	Harris et al.	710/300
*	E	US-2004/0019729	01-2004	Kelley et al.	710/306
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Intel Demos PCI Express Chips. Light Reading, 9/18/2003
	V	Intel 41210 Serial to Parallel PCI Bridge Product Brief
	W	PCI Express Extensions Beyond the Motherboard Chipset, RTC Magazine
	X	Selecting PCI Express IP for Your Design, Stephen Peltan, DesignWare Technical Bulletin

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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	V	PCI-X Bus Description, Interfacebus.com
	W	PCI-Express Bus Description, Interfacebus.com
	X	Intel RAID Controller SRCSAS18E, Product Brief

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	V	PCI Express Base Specification, Revision 1.0, pages 31-42 and 97-98, 4/29/2002
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